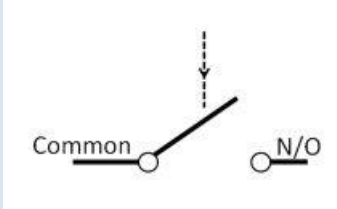
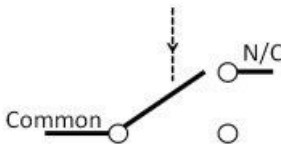
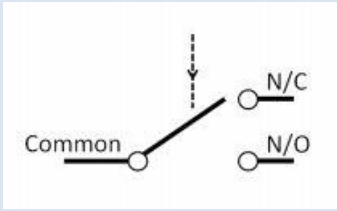
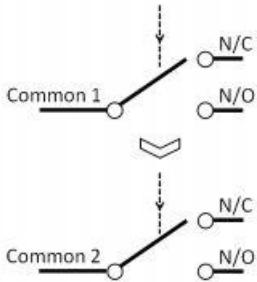
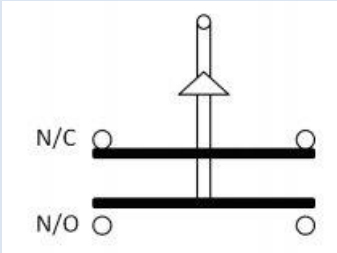
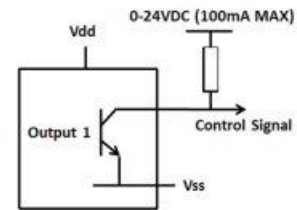


Name	Description	Symbol
<b>SPST N/O</b>	Single-Pole, Single-Throw: Normally Open	
<b>SPST N/C</b>	Single-Pole, Single-Throw: Normally Closed	
<b>SPDT</b> (Also called SPCO)	Single-Pole, Double-Throw (or Single Pole Change-Over)	
<b>SPCO + SPCO</b>	Two Single-Pole, Change-Over circuits, staggered	
<b>Single Operation</b>	Special case of Double-Pole, Single Throw Acts as one SPST N/O and one SPST N/C circuit, staggered	

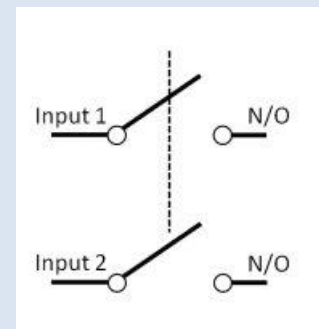
**Open collector**

Transistor output: read IFU for implementation instructions



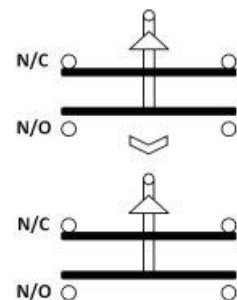
**DPST N/O**

Double-Pole, Single-Throw: Normally Open



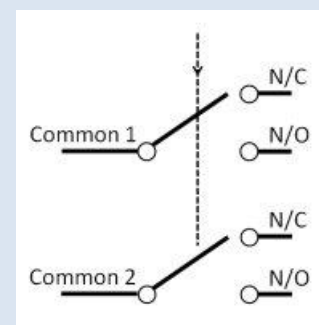
**DPST + DPST**

Two Double-Pole, Single-Throw circuits, staggered.  
Each Double-Pole, Single-Throw circuit acts as one SPST N/O and one SPST N/C circuit, staggered



**DPCO**

Double-Pole, Change-Over



**Double Operation**  
(Also called 4PST)

Two groups of Double-Pole, Single Throw circuits (Four-Pole, Single Throw).  
Each Double-Pole, Single-Throw circuit acts as one SPST N/O and one SPST N/C circuit, staggered

